	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	5425	(input adj port) and (output adj port) and buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/12/07 12:44
2	BRS	L2	29	1 and 257/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/12/07 12:41
3	BRS	L3	209	(input adj port) and (output adj port) and (signal adj buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/12/07 12:51
4	BRS	L4	391613	3 and (path adj length) clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/12/07 12:52
5	BRS	L5	3	3 and (path adj length) and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/12/07 12:57

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	BRS	L6	(87) *1st patent good one*	1 and (clock adj skew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/12/07 12:59